

EC LAYOUT DIAGRAM 原理图: (FIG-1)

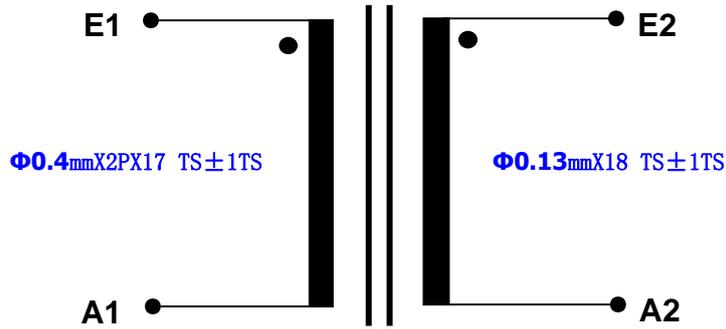


FIG-1

DATA SPECIFICATION (特性要求): (TABLE-1)

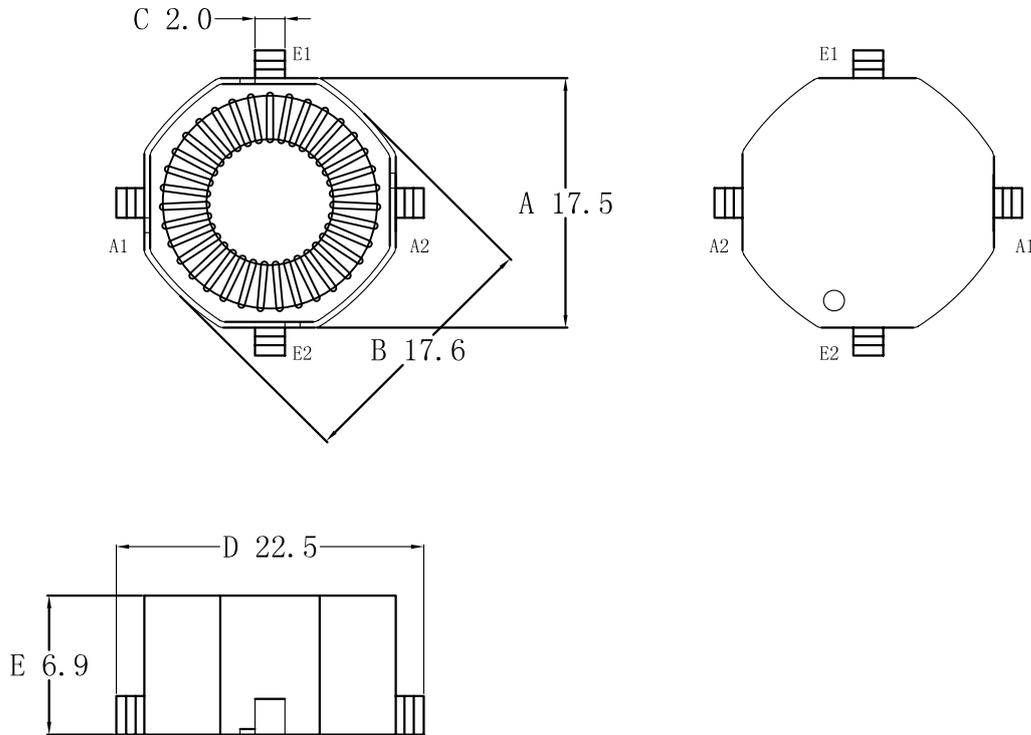
ITEM 项目	MEASUREMENTS 测试脚位	CHARACTERISTICS 测试范围	MEASURING CONDITIONS 测试条件 T=25°C ± 3; HR (%)=45 ± 15	REMARK 备注
LS 电感	PIN (E1-A1)	16. 2uH ± 10%	F (kHz)=10 Ug (V)=1	SERIAL MODE 串联模式
	PIN (E2-A2)	18. 2uH ± 10%	F (kHz)=10 Ug (V)=1	SERIAL MODE 串联模式
HIPOT 耐压	(E1-A1) / (E2-A2)	NO BREAKDOWN 漏电流 ≤ 1. 0mA	VAC=1000V /1SEC. F (Hz) =60	/
PH 相位	REFER TO LAYOUT DIAGRAM (FIG-1)			

Prepared By ENG:	Date:	TITLE: SMD POWER INDUCTOR	CUSTOMER Approved: Date:	MODEL NO. :TBP4XXX-01	
Checked By QA:	Date:			REV.	00
Approved By ENG:	Date:			PAGE	1 OF 2



OUTLINE DIAGRAM (外观图) :

UNIT : mm
Tolerance unless otherwise specified: ± 0.3



NOTE:

1. Fix the coil and case with epoxy .
用胶将coil 和case固定在一起.
2. The part should be complied with ROHS requirement
产品应满足ROHS要求

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Approved By ENG:	Date :			PAGE 2 OF 2		

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